

# Quiz 2 (Online Section)

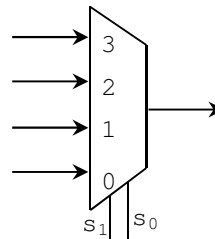
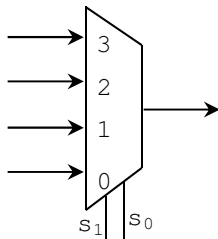
(October 3rd @ 9:30 am)

## PROBLEM 1 (20 PTS)

- Implement the following functions using i) decoders (and gates) and ii) multiplexers:
  - ✓  $F = \overline{X + Y + Z} + Y \oplus Z$
  - ✓  $F(A, B, C, D) = \prod(M_0, M_8, M_{11}, M_{15})$

## PROBLEM 2 (25 PTS)

- Using only a 4-to-1 MUX, implement the following functions.
  - ✓  $F(X, Y, Z) = \sum(m_2, m_3)$
  - ✓  $F(X, Y, Z) = \sum(m_1, m_3)$



## PROBLEM 3 (30 PTS)

- Perform the following operations using the 2's complement representation. For each case, provide the summands and the result in 2's complement representation. Use the minimum number of bits to represent the summands and the result so that overflow is avoided.
  - ✓  $-35 + 256$
  - ✓  $-26 + 128$
  - ✓  $-64 - 511$
  - ✓  $127 + 150$

## PROBLEM 4 (25 PTS)

- Complete the timing diagram of the circuit shown below:

